

# **CURRICULUM VITAE**

**PUSHKAR PRAVEEN**

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## **PROFESSIONAL EXPERIENCE (6 YEAR)**

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- Presently working at “G B Pant Institute of Engineering & Technoogy, Pauri” as Assistant Professor from 22 April 2019.
- Worked at “L&T Power Development Ltd as “Consultant- Instrumentation and providing advisory services to L&T Hydroelectric Project, Singoli-Bhatwari, Uttrakhand” from 07 March 2018 to 20 April 2019.
- Worked at “JRE Group of Institution, Greater Noida” as Assistant Professor in ECE department from February 2016 to June 2017.
- Worked at “Ram-eesh Institute of Engineering & Technology, Greater Noida” as Assistant Professor in ECE department from August 2013 to Feb 2016.
- Worked at “Institute of Technology & management, meerut” as Assistant. Professor in ECE department from January 2013 to July 2013.
- Worked at “Satyam College of Engineering, Ghaziabad” as Assistant. Professor in ECE department from August 2012 to January2013.

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## **ACADEMIC CREDENTIALS**

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- Pursuing Ph.D in Low Power VLSI Design from Uttrakhand Technical University, Dehradun.
- M.Tech(2012) in VLSI Design from Guru Gobind Singh Indraprastha University, Newdelhi
- GATE 2010 Qualified.
- B.Tech(2009) in Electronics & Telecommunication from Biju Patnaik University of Technology, Rourkela.

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## **RESEARCH PAPER NATIONAL/ INTERNATIONAL JOURNAL/CONFERENCE**

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- Software Implementation of RSA, IJATER, Volume 2, Issue4, July 2012.
- Implementation of RSA algorithm on FPGA, IJERT, Volume 1, Issue 5, July 2012.
- Hardware Implementation of RSA algorithm, ICECHVT: International Conference on Convergence of Human Values and Technology, January 2013.
- Design and Comparative Study of CMOS Charge Pump for Low Voltage Application using CTS Logic, IJECT Volume 4, Issue 1, Ver. 2 January to March 2013.
- Implementation of High Speed and Low Power Hybrid Adder Based Novel radix 4 Booth Multiplier, CSNT 2013: International Conference on Communication System and Network Technologies, IEEE Xplore Digital Library, April 2013.
- Ant Colony algorithm for Rectilinear Steiner Tree, National Conference on Electronics and Communication Systems, IPEC Ghaziabad, April 2013.
- 8-bit ALU using 16:1 multiplexer, National Conference NCECS Ghaziabad, April 2013.
- TCP Congestion Control Algorithm and its Variants in Wireless Network- A survey, RIET-IJSET, Volume 1, Issue 1, March 2014.
- Design and Implementation of 1-byte memory using 12T SRAM, RIET-IJSET, Volume 1, Issue 1, March 2014.
- Design and Implementation of Tree Decoder for SDR Applications, IEEE conference, CIPECH 2014, IEEE Xplore Digital Library, November 2014.

- Implementation of SHA on FPGA, IJCSE, Volume 3, Issue 5, May 2015.
- Conical Shape Dielectric Resonator Antenna for Ultra Wide Band Applications, IEEE conference, ICCCA 2015, IEEE Xplore Digital Library, July 2015.
- Characterization of 6T & 9T SRAM for 32nm CMOS, Institute of Research Engineers and Doctors, USA, April 2018. ISBN: 978-1-63248-152-8 DOI: 15224/978-1-63248-152-8-41.

### **CONFERENCE/ SEMINAR/ WORKSHOP/ SHORT TERM COURSES**

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- Attended workshop on “VLSI Chip Design” at CDAC Noida, on 20<sup>th</sup> June 2011.
- Attended workshop on “TI Embedded processor platforms” in cooperation with Texas Instruments India university program at CDAC Noida, on 30<sup>th</sup> January 2012.
- Participated in the “International Conference ICECHVT-2013” held on January 18-19, 2013, Organized by School of Engineering & technology, Ansal University, Gurgram.
- Participated in the national conference on Electronics and Communication Systems-2013 held on 5<sup>th</sup> -6<sup>th</sup> April 2013 at Indraprastha Engineering College, Ghaziabad.
- Attended workshop on “XILINX FPGA Solution for DSP and Embedded system application with demonstration of ANALOG DISCOVERY KIT” at BVCOE Newdelhi on 1<sup>ST</sup> SEPT 2014.
- Participated in AICTE recognized short term course on “Repair and maintenance of power Supplies and its Components” from 23<sup>rd</sup> March-27<sup>th</sup> march 2015, organized by NITTTR, Chandigarh.
- Participated in “FDP on ICT technology Trends & Skill Development” held at United Group of Institutions, Greater Noida.
- Participated in “IEEE International Conference on Computing Communication and Automation- ICCCA 2015” held at Galgotia University on 15<sup>th</sup>-16<sup>th</sup> May 2015.
- Participated in “IOT Based Air Mouse” in DST sponsored International Conference on “Advancement in Energy, Drive and Control-2017” at ITS Engineering College, Greater Noida, during April 7-8, 2017. This paper has been selected for Springer Proceeding.
- Attended a AKTU Sponsored Faculty Development Program on “Analog & Embedded System” from 17<sup>th</sup> – 22<sup>nd</sup> July 2017 at ABESIT, Ghaziabad.
- Attended a six day technical skill enhancement workshop on “SRAM Design” from 5<sup>th</sup> – 10<sup>th</sup> October 2017 at Bharati Vidyapeeth College of Engineering, Newdelhi.
- Participated in “**International Conference ICATACE-2018**” held on April 6<sup>th</sup> -7<sup>th</sup> , 2018, Organized by B.T. Kumaon Institute of Technology, Dwarahat.
- Attended AKTU Sponsored Faculty Development Program on “Power Electronics in Renewable Energy” from 8<sup>th</sup> – 12<sup>nd</sup> July 2019 at ABESEC, Ghaziabad.

### **ACTIVITIES**

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- Member of IEEE Society, membership number: 93299329.
- Member of IAENG (International Association of Engineers) Membership No: 158388
- Member of IRED, Membership No: AM1010002014
- Worked as Managing Editor in RIET-International Journal of Science, Engineering & Technology having ISSN: 2348-7364.

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