

Expert's Lecture

By Senior PhD Scholar, EED, I.I.T. Delhi

Speaker

Mr. Nishant Kumar

Senior PhD Scholar, EED, I.I.T. Delhi

Venue: Seminar room, EED

1. Maximum Power extraction techniques for Solar PV Array

Date & Time: 6th May 2019, 2:30 to 4:00 P.M.

2. Adaptive control techniques for Grid Integrated Solar PV array

Date & Time: 7th May 2019, 09:30 to 11:00 A.M.

About the speaker:



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Mr. Nishant has completed his MTech from NIT Durgapur, West Bengal, India in 2013, wherein he was a gold medalist. He has completed his BTech degree with Honors from Silicon Institute of Technology Bhubaneswar, Orissa, India in 2011. He has published 5 Patents and a total of 33 papers including 15 IEEE Transactions, 4 IET Journal. He has received many awards and honors including the POSOCO PhD thesis national award (PPSA-2018) with a cash prize of 1 Lakh. He has involved in multiple international exposures for the various research activities; Imperial College London, MIT USA, University of Strathclyde, Scotland, University of Warwick, UK. Mr. Nishant has also delivered a presentation in the Ministry on the future of Indian power grid. Currently, Mr. Nishant is waiting for his PhD defense in this month before leaving abroad for postdoc.